

Analog Input Module (AIM)

Fully Qualified Safety-Related Digital Platform

**CURTISS-
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About

Curtiss-Wright Nuclear has partnered with Radics, LLC to supply integrated FPGA-based instrumentation and control (I&C) systems for nuclear power plants and research reactors. RadICS is a digital I&C platform that is robust, flexible, and scalable. It provides state-of-the-art functions, services, and safeguards for safety applications in the nuclear industry. The RadICS product line consists of a Logic Module, basic input/output modules, and specialty modules all housed in a seismically qualified chassis.

The Analog Input Module (AIM) serves as a high-density analog field sensor acquisition module. It provides for 32 independent, highly reliable, and galvanically isolated inputs with built-in filtering, A/D conversion, and calibration to be used by the Logic Module. The AIM also performs robust and continuous self-diagnostics to ensure the safety and integrity of each input and module function.

Analog Input Module (AIM)

- High density 32 channel analog inputs with built-in Analog to Digital conversion hardware redundancy and self-diagnostics for highly reliable operation, filtering, calibration, and random hardware failure detection.
- Independent FPGA for analog input processing, self-diagnostics, and fail-safe functional behavior.
- IEC 61508 SIL 3 certification in single and multiple channel configurations.
- Robust self-diagnostics ensure high reliability and early fault detection with safety-focused fault management.
- Segregation of in processing, self-diagnostics, and watchdog functions assures safety-critical functionality.
- Galvanic isolation for signal inputs with robust and dedicated communication links to Logic Module for secure data transfer.
- Inherent on-board diversity features eliminate common cause failure vulnerabilities.
- FPGA technology ensures cyber security and resilience to obsolescence.



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Analog Input Module Technical Specifications

Function	Specifications
Input Analog Signal Range	0 to +5.1 V (0 to 20 milliamps using external 250 ohm resistor installed in connection/ junction box) Differential input impedance: not less than 1 megohm
A/D Conversion Resolution	18 bits / 400,000 samples per second
Common Mode Rejection Ratio	> 86 dB
Overall Accuracy	0.04% of full scale for 0 to +5.1 V (at 25 °C) 0.04% of full scale for 4 to 20 milliamps using external resistor with 0.05% tolerance (25 °C)
Input Channel Isolation	all input channels are galvanic-isolated up to 500 V _{RMS} AC or 700 VDC field-to-chassis and channel-to-channel
Overvoltage Protection	±60 VAC/VDC continuous (using external protection elements installed in Chassis)
Information Package Exchange Cycle	5 milliseconds
Diagnostic Package Exchange Cycle	5 milliseconds
LVDS Line Speed	100 megabit/second
LVDS Line Protocol	proprietary protocol with integrity checking (CRC), galvanic-isolated Tx / Rx
Self-Diagnostic Functions	diverse watchdog unit, checksum analysis, active diagnostics with internal fault detection, hardware error detection, functionally diverse continuous self-diagnostic tests, power supply fault detection
Power Supply / Consumption	2 independent inputs – 24 (18 – 36) VDC / 0.85 amp
Indications	2 status LED indicators (RUN/FAULT) 4-character dot matrix symbol-indicator for providing current operational mode, service information, and error codes
Operating Temperature	0 to 60 °C (32 to 140 °F)
Operating Humidity	5 to 90% relative humidity, non-condensing

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